

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|-------|---|--------------------|------------------|---------|------------------|
| L2 | 863 | (upgrad\$3 replac\$5 swap\$4) near3 (dimm\$1 memory adj module\$1) | US-PGPUB; USPAT | OR | OFF | 2007/03/08 15:01 |
| L3 | 55 | (hotplug\$4 hotswap\$4 hot adj plug\$3) near3 (dimm\$1 memory adj module\$1) | US-PGPUB; USPAT | OR | OFF | 2007/03/08 15:08 |
| L5 | 768 | "serial presence detect" | US-PGPUB; USPAT | OR | OFF | 2007/03/08 15:09 |
| L6 | 84 | (2 3) and (5 spd) | US-PGPUB; USPAT | OR | OFF | 2007/03/08 15:11 |
| L7 | 100 | (2 3) same (clock\$3 frequency speed) | US-PGPUB; USPAT | OR | OFF | 2007/03/08 15:20 |
| L8 | 56790 | multiprocessor multicore (dual multi\$3) adj (processor\$1 core\$1) | US-PGPUB; USPAT | OR | OFF | 2007/03/08 16:21 |
| L9 | 156 | (2 3) and 8 | US-PGPUB; USPAT | OR | OFF | 2007/03/08 15:21 |
| L10 | 11 | (2 3) same 8 | US-PGPUB; USPAT | OR | OFF | 2007/03/08 15:29 |
| L11 | 6 | (2 3) and smp | US-PGPUB; USPAT | OR | OFF | 2007/03/08 15:45 |
| L12 | 6825 | smp symmetric adj (multiprocessor mult\$3 adj processor\$1) | US-PGPUB; USPAT | OR | OFF | 2007/03/08 15:49 |
| L13 | 116 | 12 same slave\$1 | US-PGPUB; USPAT | OR | OFF | 2007/03/08 15:54 |
| L15 | 2 | 12 and (halt\$3 stop\$4 suspend\$3) near2 slave\$1 | US-PGPUB; USPAT | OR | OFF | 2007/03/08 15:55 |
| L16 | 6 | 12 and (halt\$3 stop\$4 suspend\$3) near4 slave\$1 | US-PGPUB; USPAT | OR | OFF | 2007/03/08 15:57 |
| L17 | 62752 | (host system north) adj (controller bridge) northbridge chipset | US-PGPUB; USPAT | OR | OFF | 2007/03/08 16:11 |
| L18 | 0 | 7 same (halt\$3 stop\$4 suspend\$3) near4 (slave\$1 processor\$1) | US-PGPUB; USPAT | OR | OFF | 2007/03/08 15:58 |
| L19 | 0 | 7 and (halt\$3 stop\$4 suspend\$3) near4 (slave\$1 processor\$1) | US-PGPUB; USPAT | OR | OFF | 2007/03/08 16:02 |
| L20 | 0 | 7 and (deactivat\$3 standby hibernat\$3) near4 (slave\$1 processor\$1) | US-PGPUB; USPAT | OR | OFF | 2007/03/08 16:11 |
| L24 | 61 | mch and (halt\$3 stop\$4 suspend\$3) near4 (slave\$1 processor\$1) | US-PGPUB; USPAT | OR | OFF | 2007/03/08 16:08 |
| L25 | 1 | mch same (halt\$3 stop\$4 suspend\$3) near4 (slave\$1 processor\$1) | US-PGPUB; USPAT | OR | OFF | 2007/03/08 16:02 |
| L26 | 20 | system adj2 logic same (halt\$3 stop\$4 suspend\$3) near4 (slave\$1 processor\$1) | US-PGPUB; USPAT | OR | OFF | 2007/03/08 16:09 |
| L27 | 4 | system adj2 logic same (deactivat\$3 standby hibernat\$3) near4 (slave\$1 processor\$1) | US-PGPUB; USPAT | OR | OFF | 2007/03/08 16:11 |
| L28 | 56 | second near frequency with (17 mch system adj2 logic) | US-PGPUB; USPAT | OR | OFF | 2007/03/08 16:13 |
| L29 | 39 | second adj frequency with (17 mch system adj2 logic) | US-PGPUB; USPAT | OR | OFF | 2007/03/08 16:13 |
| L30 | 90 | chang\$3 near2 frequency with (17 mch system adj2 logic) | US-PGPUB; USPAT | OR | OFF | 2007/03/08 16:18 |
| L31 | 1271 | 713/100.ccls. | US-PGPUB; USPAT | OR | OFF | 2007/03/08 16:18 |
| L32 | 1000 | 713/600.ccls. | US-PGPUB; USPAT | OR | OFF | 2007/03/08 16:18 |

EAST Search History

| | | | | | | |
|------------|-----------|--|----------------------------|-----------|------------|-------------------------|
| <i>L33</i> | <i>4</i> | <i>(31 32) and (28 29 30)</i> | <i>US-PGPUB; USPAT</i> | <i>OR</i> | <i>OFF</i> | <i>2007/03/08 16:19</i> |
| <i>L34</i> | <i>4</i> | <i>(31 32) and (24 26)</i> | <i>US-PGPUB; USPAT</i> | <i>OR</i> | <i>OFF</i> | <i>2007/03/08 16:20</i> |
| <i>L35</i> | <i>9</i> | <i>(31 32) and (2 3)</i> | <i>US-PGPUB; USPAT</i> | <i>OR</i> | <i>OFF</i> | <i>2007/03/08 16:20</i> |
| <i>L36</i> | <i>21</i> | <i>(8 smp) same (stop adj (clock grant) stopclk)</i> | <i>US-PGPUB; USPAT</i> | <i>OR</i> | <i>OFF</i> | <i>2007/03/08 16:21</i> |